

Features

- ◆ 60Watts peak pulse power ($t_p = 8/20\mu s$)
- ◆ Uni-directional configurations
- ◆ Solid-state silicon-avalanche technology
- ◆ Capacitance: 0.45pF typical
- ◆ Low clamping voltage
- ◆ Low leakage current
- ◆ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test

Air discharge: $\pm 25KV$

Contact discharge: $\pm 20KV$

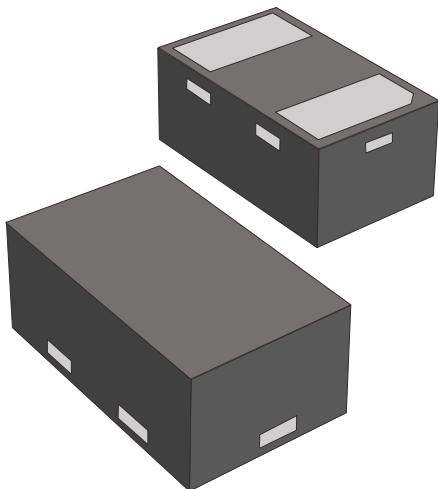
- IEC61000-4-4 (EFT) 40A (5/50ns)

- IEC61000-4-5 (Lightning) 4A (8/20us)

DFN1006

SOD-882

1mm×0.6mm×0.475mm



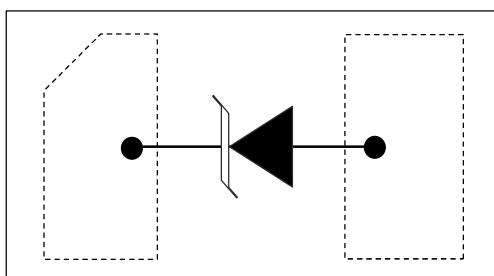
Applications

- ◆ USB 2.0 and USB 3.0
- ◆ HDMI 1.3 and HDMI 1.4
- ◆ SATA and eSATA
- ◆ DVI
- ◆ IEEE 1394
- ◆ PCI Express

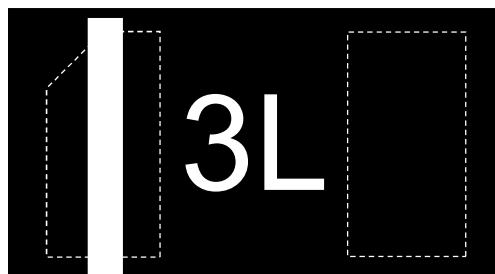
Mechanical Data

- ◆ Package: DFN1006
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Packaging: Tape and Reel
- ◆ RoHS/WEEE Compliant

Schematic & PIN Configuration



Marking



Absolute Maximum Ratings

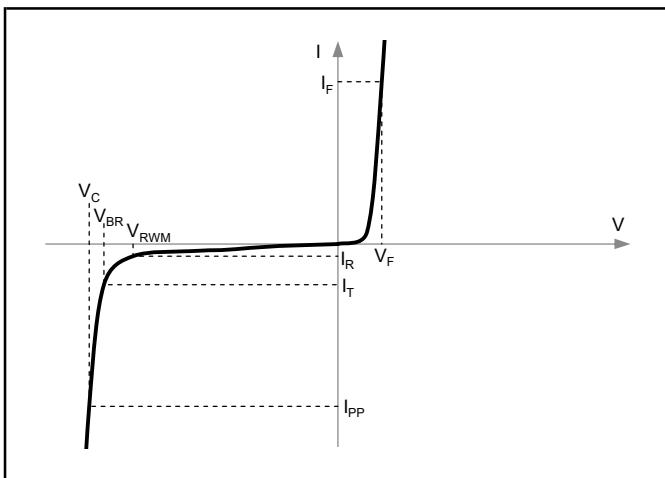
Parameter	Symbol	Value	Units
Reverse Working Voltage	V_{RWM}	3.3	V
Peak Pulse Power ($t_p=8/20\mu s$)	P_{PP}	60	W
Peak Pulse Current	I_{PP}	4	A
ESD per IEC 61000-4-2 (Air)	$V_{ESD-Air}$	25	kV
ESD per IEC 61000-4-2 (Contact)	$V_{ESD-Contact}$	20	kV
Lead Soldering Temperature (10s)	T_L	260	°C
Operating Temperature Range	T_J	-55 to 125	°C
Storage Temperature Range	T_{STG}	-55 to 150	°C

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Breakdown Voltage	V_{BR}	$I_T = 1mA$	4.2			V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3V$			0.1	uA
Forward Voltage	V_F	$I_F = 15mA$			1.2	V
Clamping Voltage	V_C	$I_{PP1} = 1A, 8/20\mu s$		8		V
Clamping Voltage	V_C	$I_{PP} = 4A, 8/20\mu s$		12	14	V
Junction Capacitance	C_J	$V_{DC} = 0V, f=1MHz$		0.45	0.8	pF

Electrical Parameters

Symbol	Definition
I_{PP}	Peak Pulse Current
V_C	Clamping Voltage
V_{RWM}	Reverse Working Voltage
I_R	Reverse Leakage Current
V_{BR}	Breakdown Voltage
I_T	Test Current
V_F	Forward Voltage
I_F	Forward Current



Typical Characteristics

Fig.1 - Peak Pulse Power Rating

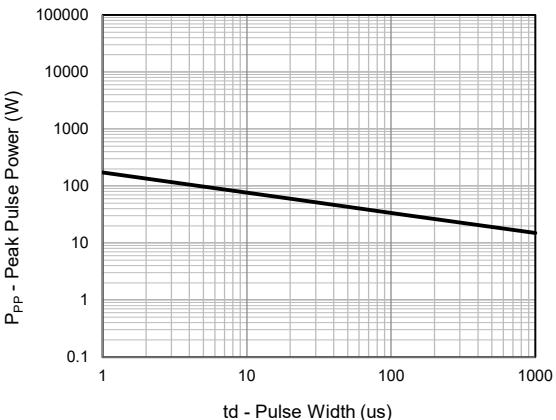


Fig.2 - Pulse Derating Curve

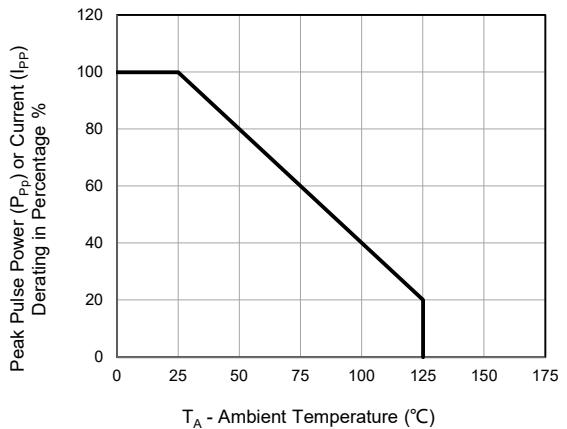


Fig.3 - 8/20us Pulse Waveform

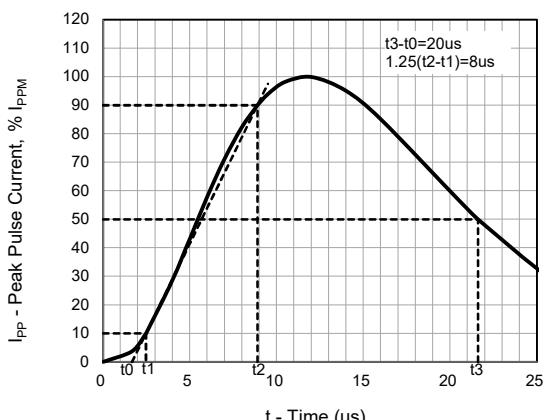


Fig.4 - Clamping Voltage

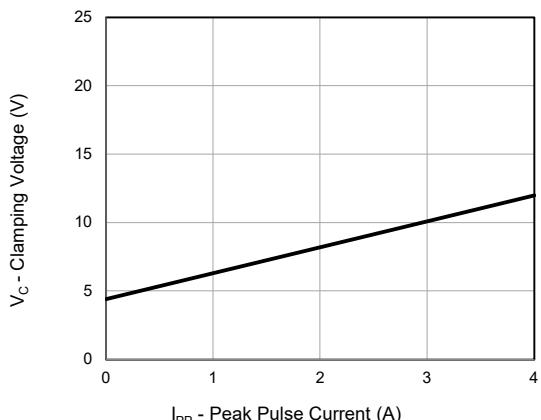


Fig.5 - ESD Pulse Waveform (IEC61000-4-2)

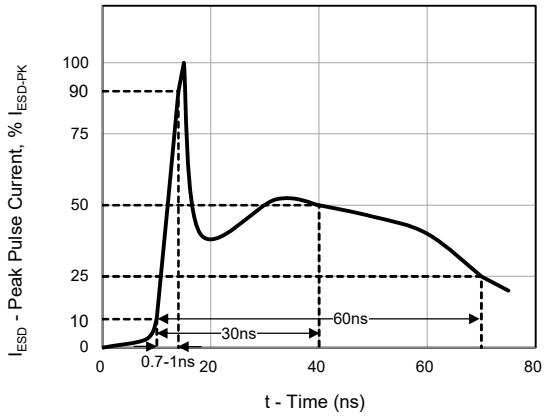
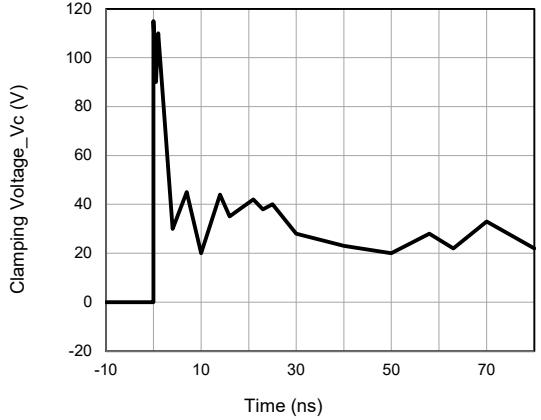
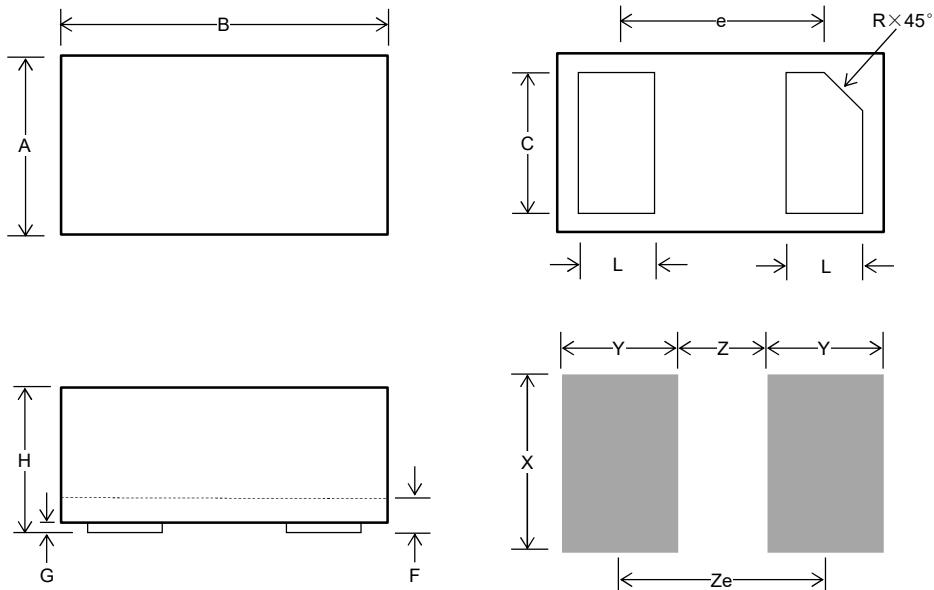


Fig.6 - ESD Clamping Voltage (8kV Contact per IEC61000-4-2)



Outline Drawing



DFN1006			
SYMBOL	Millimeters		
	MIN	NOM	MAX
A	0.55	0.6	0.68
B	0.95	1	1.08
C	0.45	0.5	0.55
F	0.12		0.18
G	-	0	0.05
H	0.45	0.475	0.5
L	0.2	0.25	0.3
e		0.65	
X		0.6	
Y		0.5	
Z		0.3	
Ze		0.8	

Ordering Information

Order Code	Package	Base Quantity	Delivery Mode
JENA6A3301SU	DFN1006	10000 PCS/Reel	Tape and Reel

Contact Information

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